

Electronic Acknowledgement Receipt

EFS ID:	2078950
Application Number:	10552948
International Application Number:	
Confirmation Number:	6753
Title of Invention:	Read and erase verify methods and circuits suitable for low voltage non-volatile memories
First Named Inventor/Applicant Name:	Jian Chen
Customer Number:	66785
Filer:	Gerald Paul Parsons/Loretta Garcia (GPP)
Filer Authorized By:	Gerald Paul Parsons
Attorney Docket Number:	SNDK.284US1
Receipt Date:	13-AUG-2007
Filing Date:	11-SEP-2006
Time Stamp:	20:48:24
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		SNDK284US1_POA-Trans.p df	561742 085e4540622b5c0bb4fbabb26d9ffd593 1df22cb	yes	3

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Miscellaneous Incoming Letter	1	1
	Power of Attorney	2	3
Warnings:			
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